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GaAs QFN 5x5mm Low Noise Amplifier 2-20GHz

#### **Features**

• Frequency: 2 - 20GHz

• Small Signal Gain: 19dB Typical • Gain Flatness:  $\pm$ 1.0dB Typical

Noise Figure: 2.5dB Typical

P1dB: 20dBm Typical

Supply voltage:

VD =+5V to +7V VG=-1V to 0 to +1V

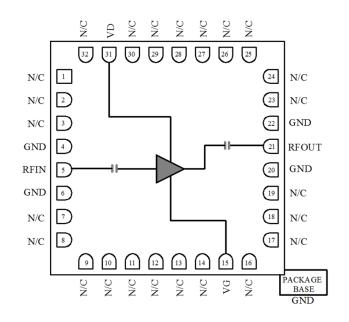
• Input/Output: 50Ω

• Package Size: 5 x 5 x 0.8 mm

## **Typical Applications**

- Test Instrumentation
- Microwave Radio & VSAT
- Military & Space
- Telecom Infrastructure
- Fiber Optics

# **Functional Block Diagram**



## **Electrical Specifications**

#### TA = +25°C, VD=+5V,VG= Suspended IDD = 113mA Typical

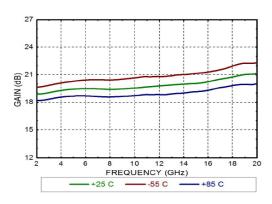
Parameters	Min.	Тур.	Max.	Units
Frequency	2-20			GHz
Small Signal Gain	17.5 19			dB
Gain Flatness		±1.0		dB
Noise Figure	2.5		dB	
P1dB - Output 1dB Compression		20		dBm
Past - Saturated Output Power	21		dBm	
OIP3 - Output Third Order Intercept		30		dBm
Input Return Loss		15		dB
Output Return Loss	15		dB	

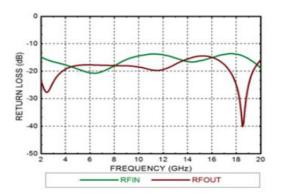
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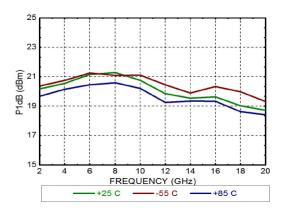
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# **Measurement Plots: S-parameters**

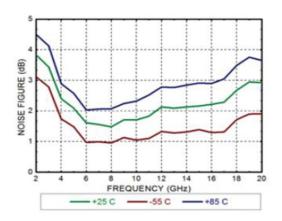




## **Measurement Plots: P1dB**



## **Measurement Plots: Noise Figure**





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### **Absolute Maximum Ratings**

Drain Bias Voltage (VD)	+8V
RF Input Power (RFIN)(VDD=+5V)	+18 dBm
Channel Temperature	175°C
Continuous Pdiss (T = 85 °C) (derate 13.3mW/°C above 85 °C)	1.2W
Thermal Resistance (channel to die bottom)	50°C/W
Operating Temperature	-55°C to +85 °C
Storage Temperature	-55°C to +150 °C

# **Typical Supply Current vs. VD**

VD (V)	IDD (mA)
+5	113
+7	130



ELECTROSTATIC SENSITIVE DEVICE OBSERVE HANDLING PRECAUTIONS

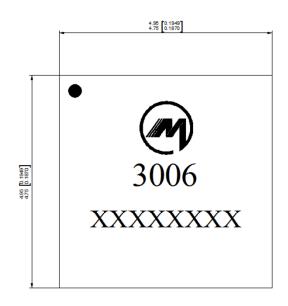
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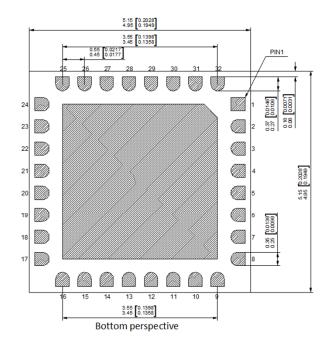


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# **Outline Drawing:**

All Dimensions in mm[inches]







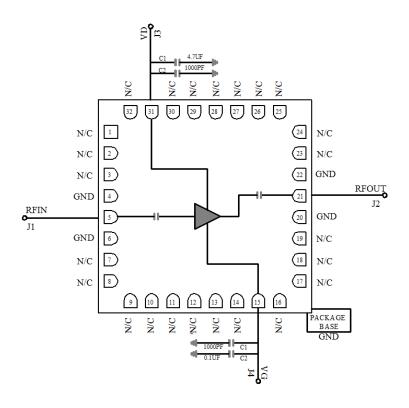
#### Notes:

- 1. Package body material : Alumina.
- 2. Lead and ground paddle plating: Gold flash over nickel.
- 3. Dimensions are in millimeters(inches).
- 4. Lead spacing tolerance is non-cumulative.



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# **Assembly Drawing**



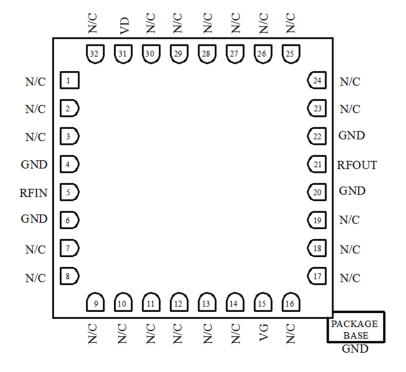
## **Pin Descriptions**

No	Function	Description
1,2,3,7,8,9,10,11,12,13,14 ,16,17,18,19,23,24,25,26, 27,28,29,30,32	NC	No connection. These pins may be connected to RF ground. Performance will not be affected.
5	RF IN	RF Signal Input. This pad is ac-coupled and matched to 50 $\Omega$ .
21	RF OUT	RF Signal Output. This pad is ac-coupled and matched to 50 $\Omega$ .
31	VD	Connect to external 1000pF and 4.7uF bypass capacitors.
15	VG	Suspended or Connect to external 1000pF and 0.1uF bypass capacitors.
4,6,20,22	GND	These pins & exposed ground paddle must be connected to RF/DC ground
	GND	Package bottom must be connected to RF/DC ground

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# **Biasing and Operation**

#### Turn ON procedure:

- Connect GND to RF and dc ground.
- 2. Set the gate bias voltages, VG set to -1.0V or suspended.
- 3. Apply drain bias voltage, VD set to +5.0 V.
- 4. Apply RF signal.

#### **Turn OFF procedure:**

- 1. Turn off the RF signal.
- 2. Turn off the drain bias voltage VD.
- 3. Turn off the gate bias voltages VG or suspended.

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