

GaAs QFN 4x4mm Low Noise Amplifier 6-18GHz

Features

Single Biasing Voltage (Self Biased)

• Frequency: 6-18GHz

Small Signal Gain: 20.5dB Typical • Gain Flatness: ± 0.5 dB Typical Noise Figure: 1.6dB Typical

• P1dB: 16.5dBm Typical Power Supply: +5V/85mA

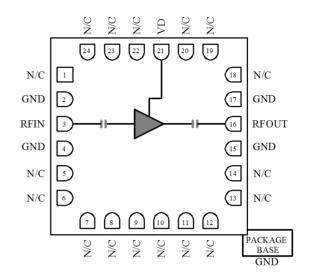
• Input/Output: 50Ω

Package Size: 4 x 4x 0.8mm

Typical Applications

- Test Instrumentation
- Microwave Radio & VSAT
- Military & Space
- Telecom Infrastructure
- Fiber Optics

Functional Block Diagram



Electrical Specifications

$TA = +25^{\circ}C$, VD = +5V, IDD = 85mA Typical

Parameters	Min.	Тур.	Max.	Units
Frequency	6		18	GHz
Small Signal Gain	19.5	20.5	dB	
Gain Flatness		±0.5		dB
Noise Figure		1.6		dB
P1dB - Output 1dB Compression	15	16.5	dBm	
Psat - Saturated Output Power		17.5		dBm
OIP3 - Output Third Order Intercept		25		dBm
Input Return Loss		-14		dB
Output Return Loss		-14	dB	

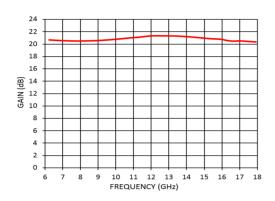
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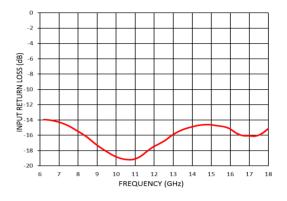


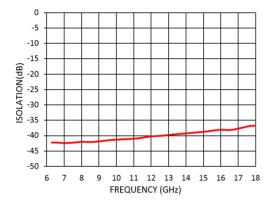
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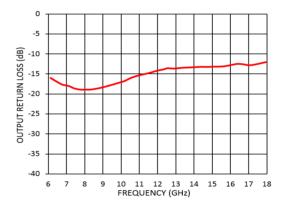
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Measurement Plots: S-parameters





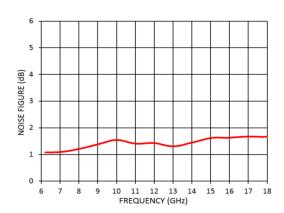




Measurement Plots: P1dB

20 19 18 17 17 18 17 19 11 14 13 12 11 10 6 7 8 9 10 11 12 13 14 15 16 17 1 FREQUENCY (GHz)

Measurement Plots: Noise Figure



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V1.0.0

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Absolute Maximum Ratings

Drain Bias Voltage (VD)	+7V
RF Input Power (RFIN)(VD=+5V)	+20dBm
Channel Temperature	175°C
Continuous Pdiss (T = 85 °C) (derate 7mW/°C above 85 °C)	0.63W
Thermal Resistance (channel to die bottom)	50°C/W
Operating Temperature	-55°C to +85 °C
Storage Temperature	-55°C to +150 °C

Typical Supply Current vs. VD

VD (V)	IDD (mA)	
+5	85	



ELECTROSTATIC SENSITIVE DEVICE OBSERVE HANDLING PRECAUTIONS

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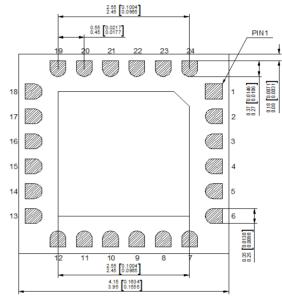
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Outline Drawing:

All Dimensions in mm[inches]





Bottom perspective



Notes:

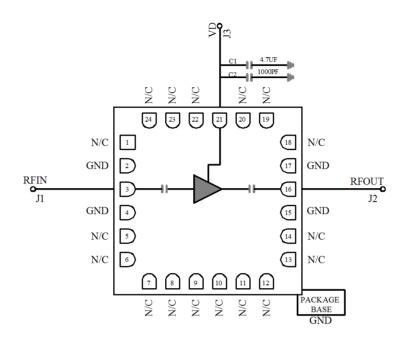
- 1. Package body material : Alumina.
- 2. Lead and ground paddle plating: Gold flash over nickel.
- 3. Dimensions are in millimeters(inches).
- 4. Lead spacing tolerance is non-cumulative.



V1 0

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Assembly Drawing



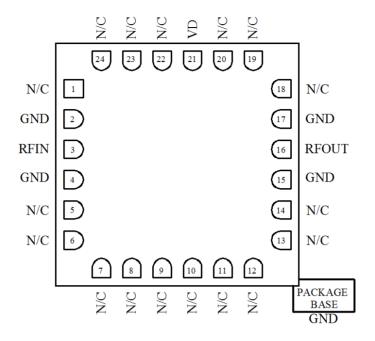
Pin Descriptions

No	Function	Description
1,5,6,7,8,9,10,11,12,13, 14,18,19,20,22,23,24	NC	No connection. These pins may be connected to RF ground. Performance will not be affected.
3	RF IN	RF Signal Input. This pad is ac-coupled and matched to 50 Ω .
16	RF OUT	RF Signal Output. This pad is ac-coupled and matched to 50 Ω .
21	VD	Connect to external 1000pf and 4.7uf bypass capacitors.
2,4,15,17	GND	These pins & exposed ground paddle must be connected to RF/DC ground
	GND	Package bottom must be connected to RF/DC ground

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Biasing and Operation

Turn ON procedure:

- 1. Connect GND to RF and dc ground.
- 2. Apply positive drain voltage VD and set to +5.0 V.
- 3. Apply RF signal.

Turn OFF procedure:

- 1. Turn off the RF signal.
- 2. Turn off the positive drain voltage VD.

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