

Features

• Single Biasing Voltage (Self Biased)

• Frequency: 20 - 40GHz

Small Signal Gain:

20.5dB@24GHz 18.5dB@40GHz Typical

• Gain Flatness: \pm 1.0dB Typical

Noise Figure: 2.7dB Typical

• P1dB: 14dBm Typical

Power Supply: +5V/70mA

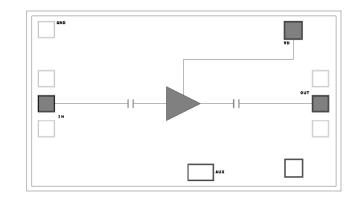
• Input/Output: 50Ω

• Chip Size: 1.85 x 1.05 x 0.1mm

Typical Applications

- Test Instrumentation
- Microwave Radio & VSAT
- Military & Space
- Telecom Infrastructure
- Fiber Optics

Functional Block Diagram



Electrical Specifications

TA = +25°C, VD = +5V, IDD = 70mA Typical

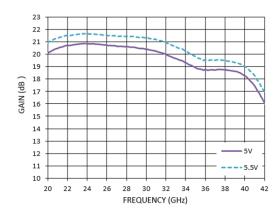
Parameters	Min.	Тур.	Max.	Min.	Тур.	Max.	Units
Frequency	20-30		30-40			GHz	
Small Signal Gain	19	20.5		17.5	19.5		dB
Gain Flatness		±0.3			±1.0		dB
Noise Figure		2.7	3.0		2.7	3.0	dB
P1dB - Output 1dB Compression	12	14		12	14.5		dBm
Past - Saturated Output Power		16			16		dBm
OIP3 - Output Third Order Intercept		21			22		dBm
Input Return Loss		12			10		dB
Output Return Loss		12			10		dB

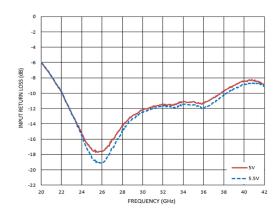
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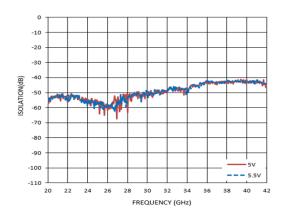
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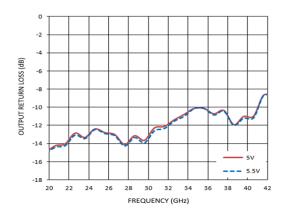


Measurement Plots: S-parameters

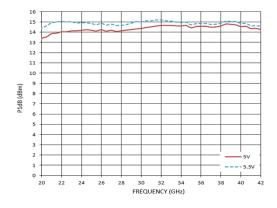




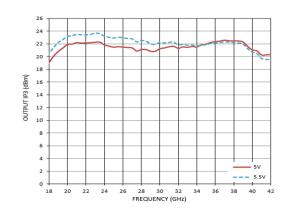




Measurement Plots: P1dB



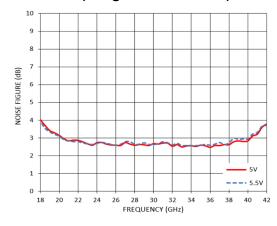
Measurement Plots: OIP3



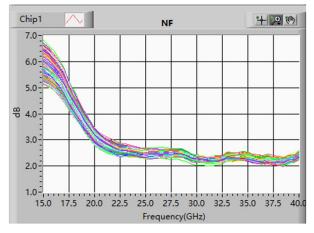
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Measurement Plots: Noise Figure (6 Sigma Worst Case)



Measurement Plots: Noise Figure (Statistic 6000pcs)



Absolute Maximum Ratings

Drain Bias Voltage (VD)	+7V	
RF Input Power (RFIN)(VDD=+5V)	+10 dBm	
Channel Temperature	150 °C	
Continuous Pdiss (T = 85 °C)(derate 24 mW/°C above 85 °C)	25dBm	
Thermal Resistance (channel to die bottom)	50°C/W	
Operating Temperature	-55 to +85 °C	
Storage Temperature	-55 to +150 °C	

Typical Supply Current vs. VD

VD (V)	IDD (mA)
+5	70
+5.5	85
+6	105



ELECTROSTATIC SENSITIVE DEVICE OBSERVE HANDLING PRECAUTIONS

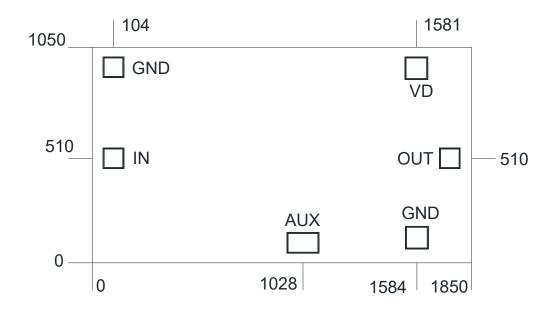
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Outline Drawing:

All Dimensions in µm

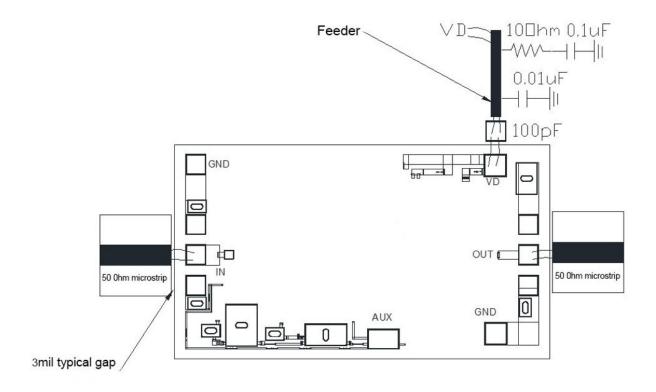


Notes:

- 1. Die thickness: 100um
- 2. DC bond pad is 100 x 100 μm^2
- 3. RF IN/OUT bond pad is 100 x 100 μm^2
- 4. DC bond pad is 100 x 100 μm²
- 5. No DC Blocking Capacitor needed for RF input & RF output
- 6. Bond pad metalization: Gold
- 7. Backside metalization: Gold
- 8. Backside of the die (GND)



Assembly Drawing



No	Function	Description	
1	RF IN	Signal input terminal, connected to 50Ω circuit	
2	RF OUT	Signal output terminal, connected to 50Ω circuit	
3	VD	Connect to external 100pF and 0.01uF bypass capacitors.	
9	GND1	Ground pad.	
10	GND2	Ground pad.	

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GND		VD
RF IN		RF OUT
	AUX	GND

Biasing and Operation

Performance is optimized when the drain voltage VD is set to +5 V.

Turn ON procedure:

- 1. Connect Input and Output with 50 Ohm source/load.
- 2. Apply positive drain voltage VD and set to +5.0 V
- 3. Apply RF signal

Turn OFF procedure:

- 1. Turn off RF signal
- 2. Turn off positive drain voltage VD

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