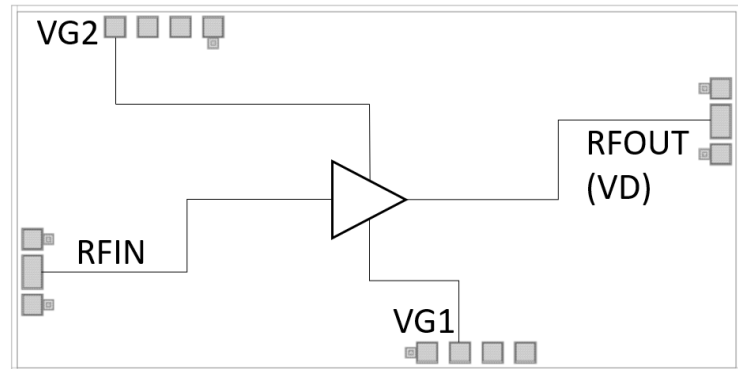




Features

- Frequency: DC-30GHz
- Small Signal Gain: 16dB
- Gain Flatness: $\leq \pm 1.0\text{dB}@DC-30\text{GHz}$
- Noise Figure: $< 4\text{dB}$
- P1dB: $>24.5\text{dBm}$, 26dBm at 10GHz
- Psat: $>26\text{dBm}$, 27.5dBm at 10GHz
- Power Supply: $+12\text{V}/220\text{mA}$
- Input/Output: 50Ω
- Die Size: $3.3 \times 1.63 \times 0.07 \text{ mm}$

Functional Block Diagram



Typical Applications

- Test Instrumentation
- Microwave Radio & VSAT
- Military & Space
- Telecom Infrastructure
- Fiber Optics

Electrical Specifications

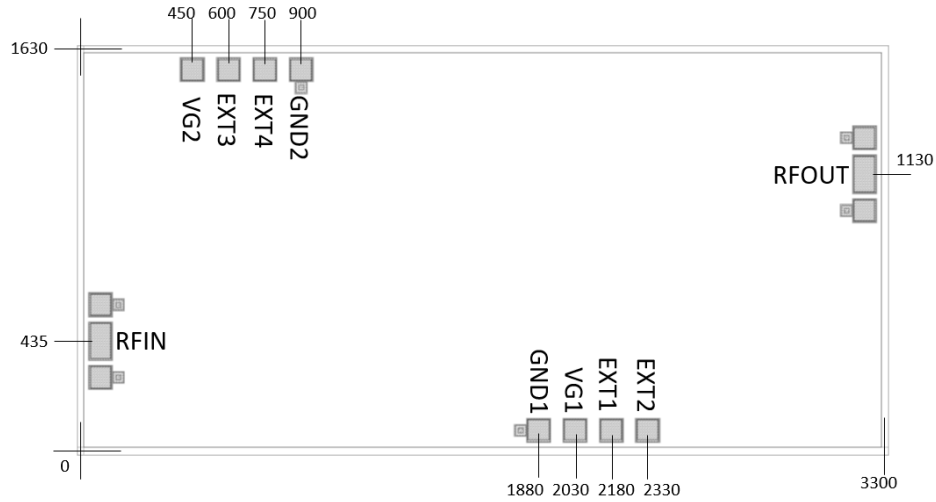
TA = +25°C, VD = +12V, VG1 = -0.4V, VG2 = 5.6V, ID = 220mA

Parameters	Min.	Typ.	Max.	Min.	Typ.	Max.	Min.	Typ.	Max.	Units
Frequency	DC-6			6-20			20-30			GHz
Small Signal Gain		17			16			16		dB
Gain Flatness		± 1			± 0.5			± 0.5		dB
Noise Figure		< 4			< 2			< 4		dB
Output 1dB Compression (P1dB)		26			26			24.5		dBm
Saturated Output Power (Psat)		27.5			27.5			26		dBm
Input Return Loss		> 10			> 10			> 10		dB
Output Return Loss		> 20			> 15			> 15		dB

* Adjust VG1, VG2 slightly to obtain device current of 220mA.



Outline Drawing:
All Dimensions in μm

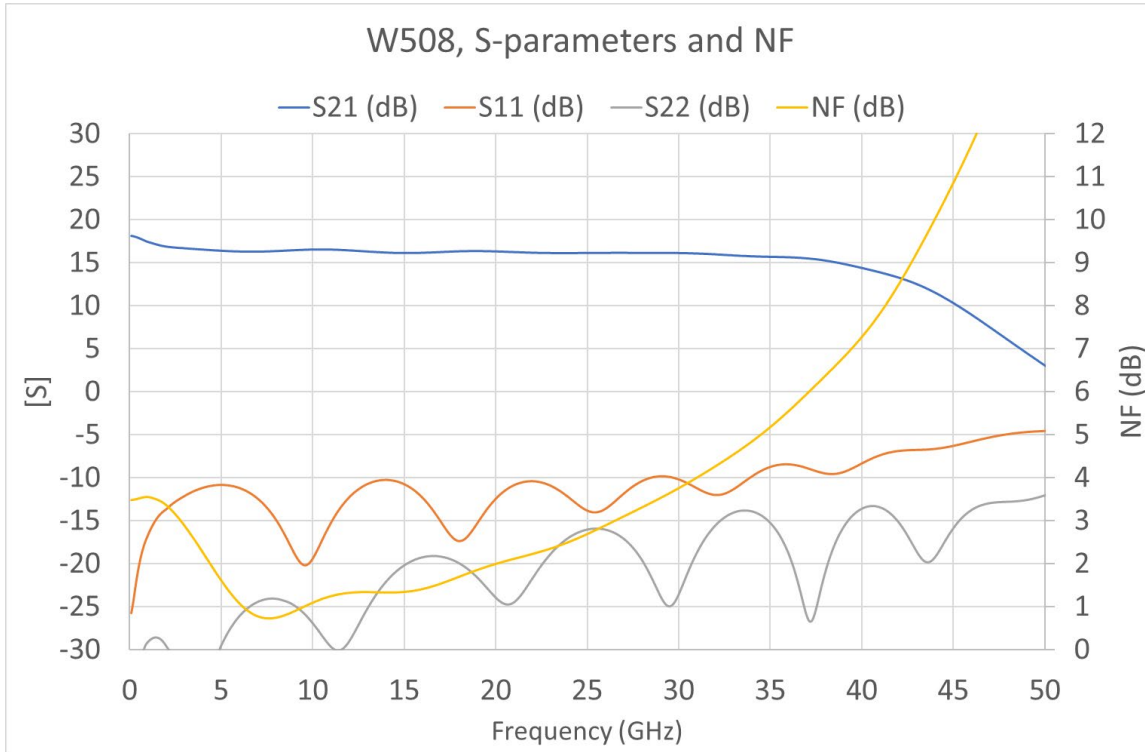


Pad Description

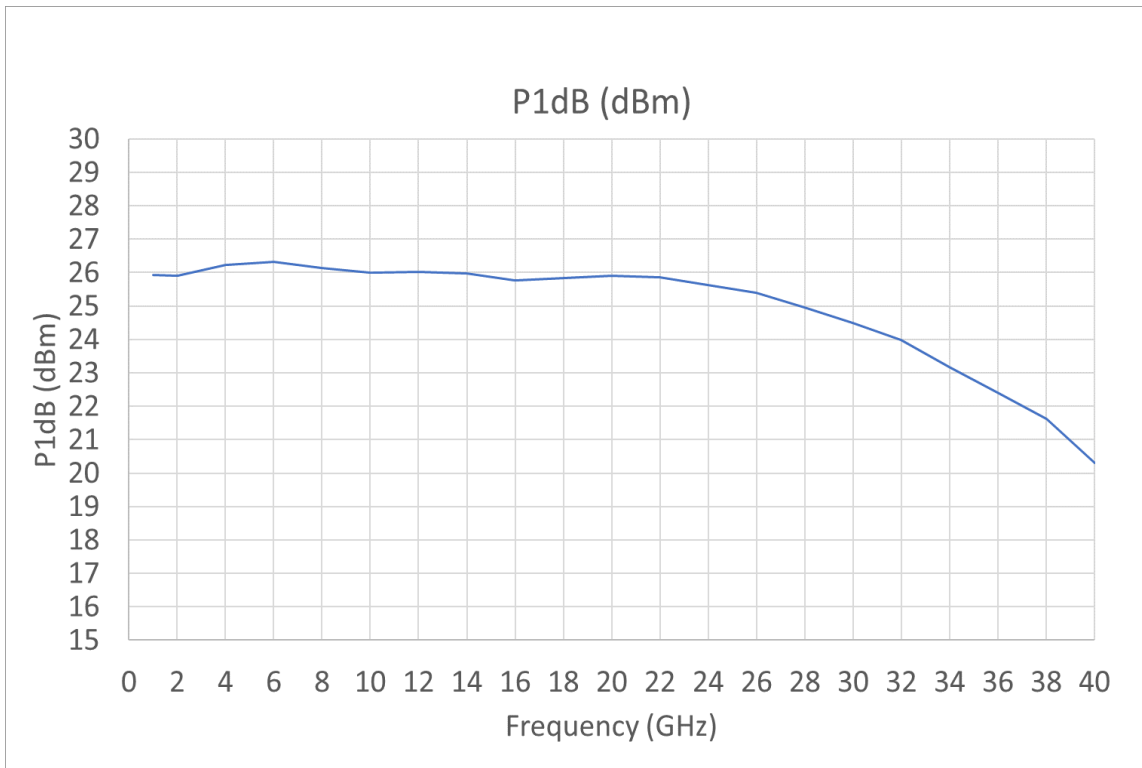
No	Function	Description
1	RF IN	Signal input terminal, connected to 50 Ω circuit; blocking capacitor required.
2	RF OUT	Signal output terminal, connected to 50 Ω circuit; blocking capacitor required; external DC biasing network required; drain current provided.
3	VG1	Amplifier 1 st gate bias; connect to external 1000pF and 0.01uF bypass capacitors.
4	VG2	Amplifier 2 nd gate bias; connect to external 1000pF and 0.01uF bypass capacitors.
5	EXT1	External bypass pad; connect to external 0.47uF bypass capacitor.
6	EXT2	External bypass pad; connect to external 1000pF bypass capacitor.
7	EXT3	External bypass pad; connect to external 1000pF bypass capacitor.
8	EXT4	External bypass pad; connect to external 0.47uF bypass capacitor.
9	GND1	Ground pad.
10	GND2	Ground pad.



Measurement Plots: S-parameters

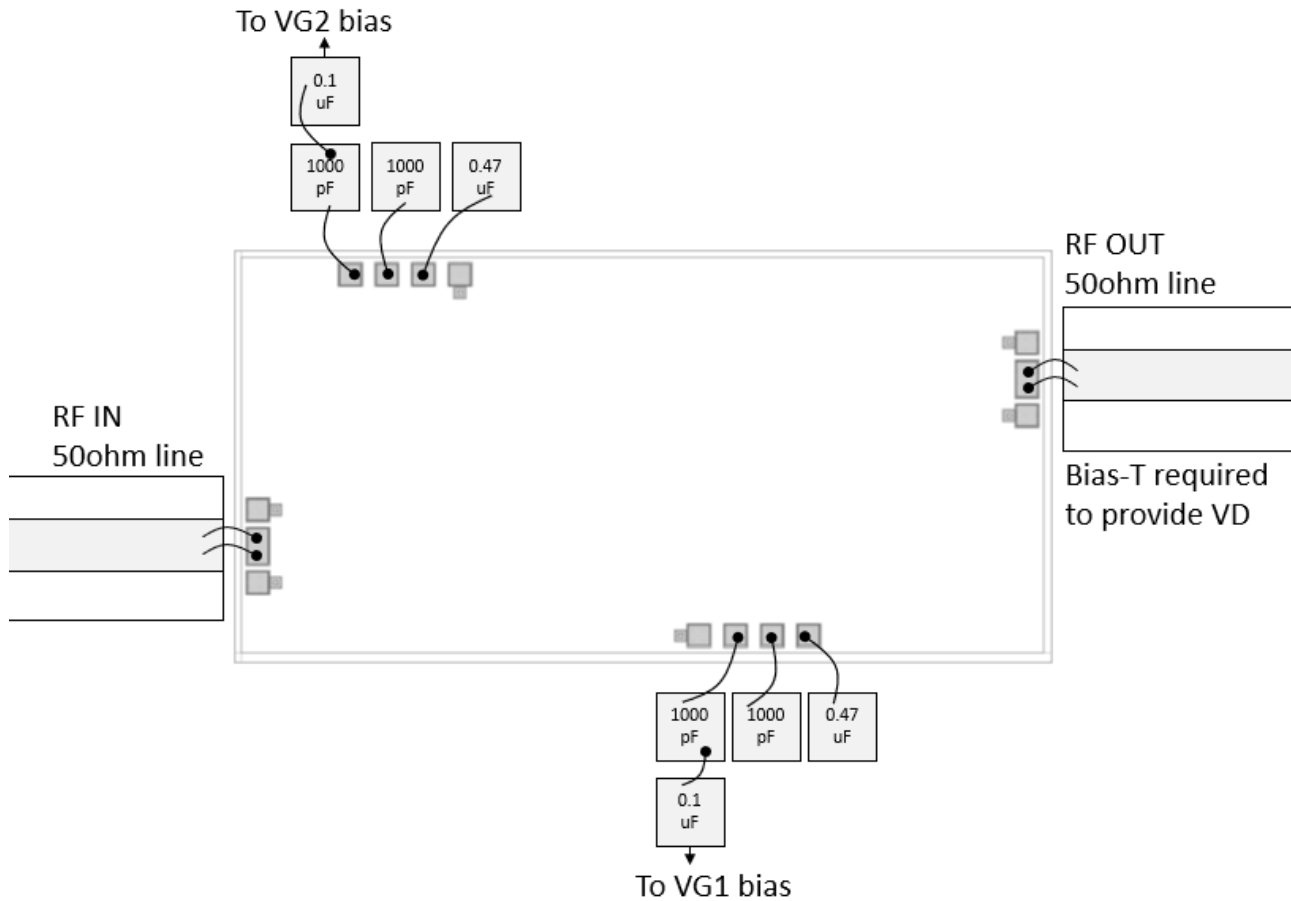


Measurement Plots: P1dB





Assembly Drawing



Notes:

1. Die thickness: 70um
2. DC bond pad is 100 x 100 μm^2
3. RF IN/OUT bond pad is 100 x 160 μm^2
4. Bond pad metalization: Gold
5. Backside metalization: Gold
6. Backside of the die (GND)